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Term	Documents
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<u>L20</u>	L16 and l6	1	<u>L20</u>
<u>L19</u>	L16 and l5	2	<u>L19</u>
<u>L18</u>	L16 and l4	5	<u>L18</u>
<u>L17</u>	L16 and l3	119	<u>L17</u>
<u>L16</u>	L15 and microprocessor\$1	2908	<u>L16</u>
<u>L15</u>	(assign\$7 or designat\$6 or sel or select\$7 or mux or multiplex\$6) near8 resourc\$4 and l1	11311	<u>L15</u>
<u>L14</u>	l2 and l7	0	<u>L14</u>
<u>L13</u>	l2 and l6	0	<u>L13</u>
<u>L12</u>	l2 and l5	0	<u>L12</u>
<u>L11</u>	l2 and l4	0	<u>L11</u>

<u>L10</u>	l2 and l3	13	<u>L10</u>
<u>L9</u>	l2 and (assign\$7 or designat\$6 or sel or select\$7 or mux or multiplex\$6)	72	<u>L9</u>
<u>L8</u>	l2 and (assign\$7 or designat\$6 or sel or select\$7 or mux or multiplex\$6) near8 resourc\$4	28	<u>L8</u>
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<u>L7</u>	(718/104)[CCLS]	897	<u>L7</u>
<u>L6</u>	(714/728)[CCLS]	109	<u>L6</u>
<u>L5</u>	(711/216,217)[CCLS]	645	<u>L5</u>
<u>L4</u>	(712/32, 227)[CCLS]	363	<u>L4</u>
<u>L3</u>	(712/2-300)[CCLS]	11115	<u>L3</u>
<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>			
<u>L2</u>	L1 near12 microprocessor\$1	78	<u>L2</u>
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IEEE JNL IEEE Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IEEE CNF IEEE Conference Proceeding

IEEE STD IEEE Standard

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 International Conference on  
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